

REMARKSPending Claims

Claims 2, 7, 8, and 13 have been amended; and claim 14 has been added. Accordingly, claims 1-11, 13 and 14 are currently pending.

Discussion of Amendments

Applicants have amended claims 2 and 8 to specify that the predetermined standard of the interface unit is IEEE1394. Additionally, claim 14 has been added as a dependent claim that depends from claim 13 and sets forth the same limitation. The specification supports the IEEE1394 standard limitation, as claimed. See page 15, line 15 to page 17, line 9 of the specification, for example.

Claim 7 has been amended to include claim language that was inadvertently omitted from the last submitted claim listing. Additionally, claim 7 has been amended to include that the CPU sets an encryption key for the encryption unit to a key register, a first control value to a first control register of the lower-layer interface unit via a bus coupled to the CPU, and a second control value to a second control register of the upper-layer interface unit via the bus. The

encryption unit, lower-layer interface unit, upper-layer interface unit, and key generation unit are claimed as being formed on a single chip, and the standard of the lower-layer interface unit and of the upper layer interface unit is claimed as being a predetermined standard, which provides antecedent basis for the amendment made to claim 8.

Conclusion

Applicants respectfully request entry of the foregoing amendments. Accordingly, reconsideration and reexamination are requested.

Respectfully submitted,



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